

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. ....09/167,174  
Priority Filing Date ..... October 6, 1998  
Inventor..... Aftab Ahmad, et al.  
Assignee..... Micron Technology, Inc.  
Priority Group Art Unit .....2811  
Priority Examiner ..... O. Nadav  
Attorney's Docket No. ....MI22-1885  
Title: Semiconductor Transistor Devices and Methods for Forming Semiconductor  
Transistor Devices

Assistant Commissioner for Patents  
Washington, D. C. 20231  
Attention: Official Draftsman

**LETTER SUBMITTING FORMAL DRAWINGS**

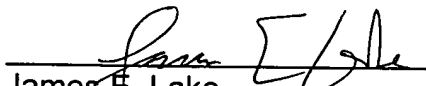
Please enter the enclosed formal drawings in the above-referenced application in place of the originally filed drawings.

The proposed changes are indicated in red on attached copies. The formal drawings incorporate the proposed changes. Acknowledgment of receipt of the formal drawing sheets and their acceptance into the file is requested.

Respectfully submitted,

Date: 29 Nov 2001

By:

  
James E. Lake  
Reg. No.: 44,854

Enclosures: Twelve (12) sheets of redline drawings and Twenty (20) sheets of formal drawings, Figs. 1-38



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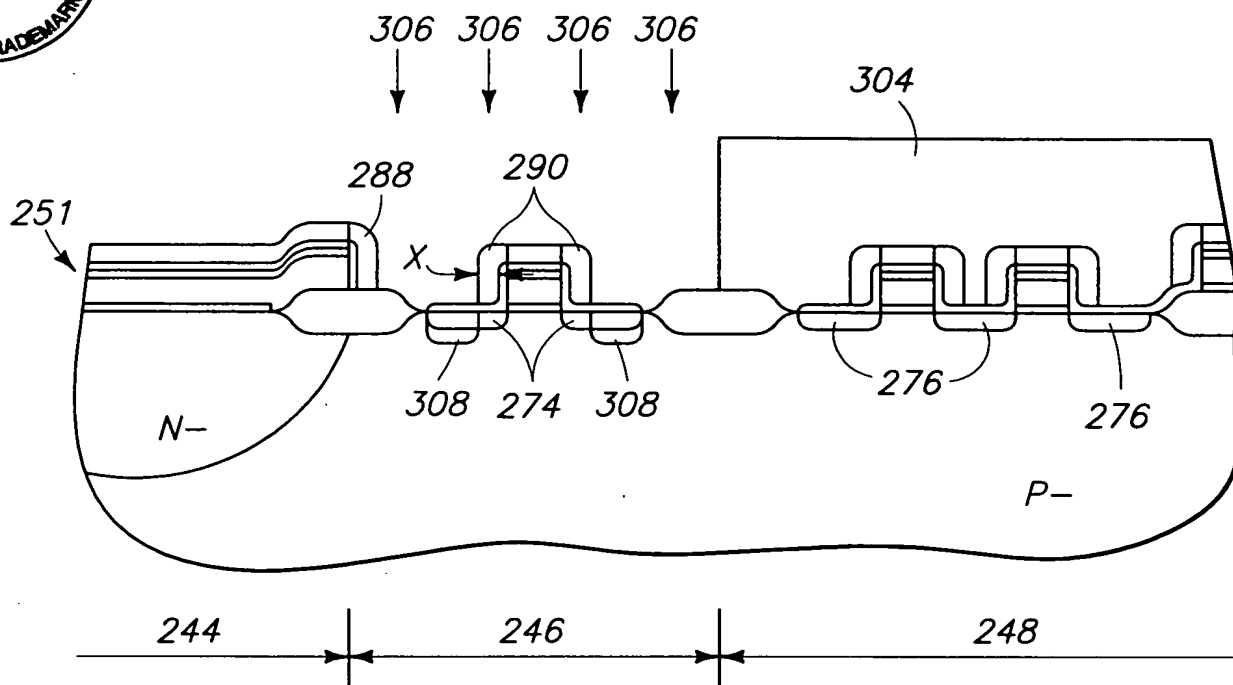


Fig. 15/20

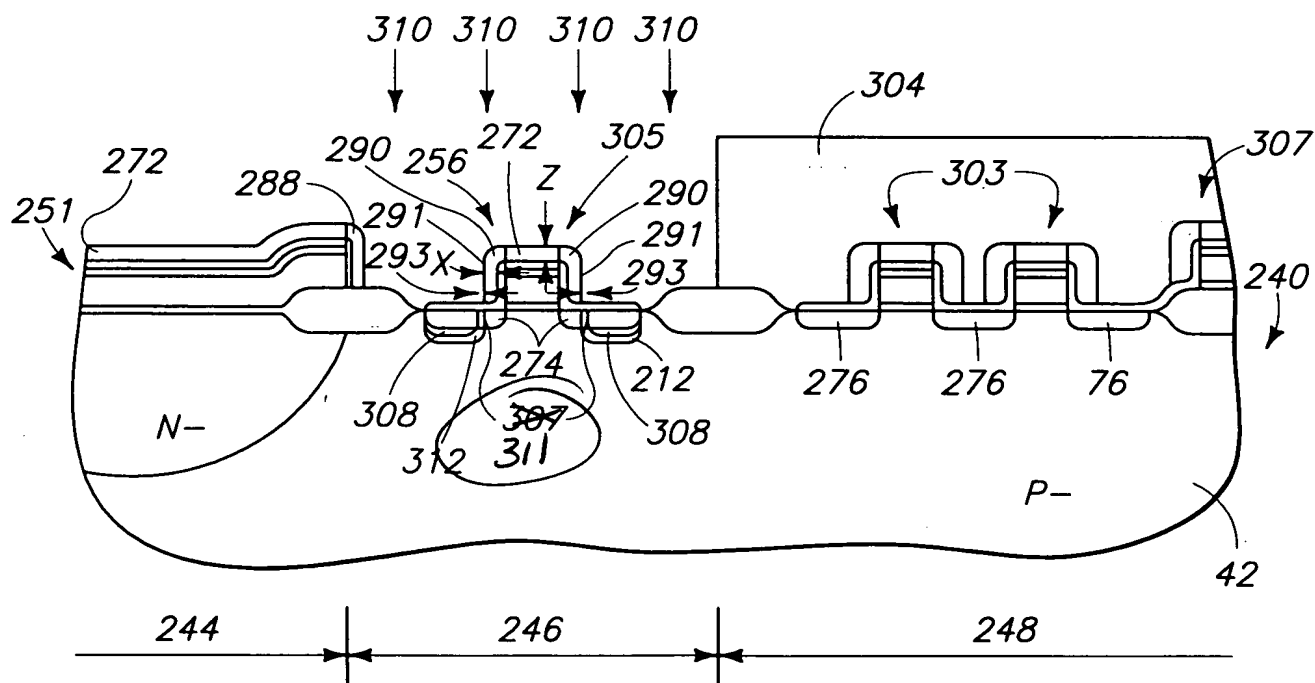


Fig. 15/21